

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KAJIHARA, et al.
Serial No.: 09/328,910
Filed: June 9, 1999
For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE USING THE SAME AND METHOD OF AND
PROCESS FOR FABRICATING THE TWO
Group: 2815
Examiner: J. Clark
Conf. No.: 7455

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

July 7, 2008

Sir:

In response to the Office Action dated June 4, 2008, please amend the
above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims

Remarks are included following the amendments